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Substitute for PTO Form 1449A-0102

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 4

Complete if Known

Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Richard E. Perego
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-5,953,215*	09-14-1999	Karabatsos	
		US-6,142,830*	11-07-2000	Looeffler	
		US-6,185,644 B1*	02-06-2001	Farmwald et al.	
		US-6,266,252 B1*	07-24-2001	Karabatsos	
		US-6,317,352 B1*	11-13-2001	Halbert et al.	
		US-6,414,904 B2*	07-02-2002	So et al.	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

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Sheet **2**of **4****Complete if Known**

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		Number-Kind Code ² (if known)			
		US-5,034,917*	07-23-1991	Bland et al.	
		US-4,977,498*	12-11-1990	Rastegar et al.	
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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/766,131
		Filing Date	January 28, 2004
		First Named Inventor	Richard E. Perego
		Art Unit	Unknown
		Examiner Name	Unknown
Sheet 3 of 4	Attorney Docket Number	RAMB-01049US2	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		*Intel Developer Forum, "DDR2 Memory In 2004 Servers - Recipes For Successful Designs", September 2003	
		*QBM Alliance, Platform Conference, "Quad Band Memory (QBM tm): DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002	
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		*CATALDO, A., "TI Fields DSP-Based Media Processor on a DIMM", EE Times (Jan. 2000)	
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		*JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				<i>Application Number</i>	10/766,131
				<i>Filing Date</i>	January 28, 2004
				<i>First Named Inventor</i>	Perego
				<i>Art Unit</i>	2188
				<i>Examiner Name</i>	
				<i>Attorney Docket Number</i>	RAMB-01049US2
Sheet	4	of	4		

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Sheet

4

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First Named Inventor	Richard E. Perego
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS

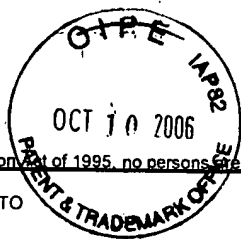
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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		**Draft Standard for a High-Speed Memory Interface (Synclink)", Draft 0.99 IEEE P1596.7-199X, IEEE Inc., pp. 1-51 (1996)	
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		*Minutes of meeting no. 75 JEDEC JC-42.3 Committee on RAM Memories, and referenced attachment "Y" of the presentation titled "Synclink", May 24, 1995, New Orleans	
		*Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Server Memory Architecture: Capacity, Performance, Reliability, and Longevity", February 18, 2004	
		*Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004	

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Sheet 1

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Application Number	10/766,131
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		Number-Kind Code ² (if known)			
		US-6690726*	02-10-2004	Yavits et al.	
		US-5325493*	06-28-1994	Herrell et al.	
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10/766.131

Filing Date

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First Named Inventor

Perego

Art Unit

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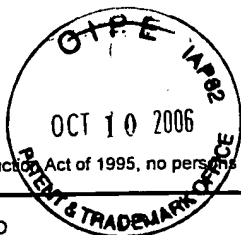
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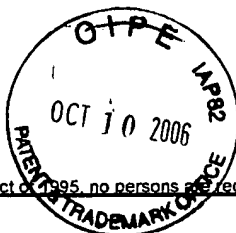
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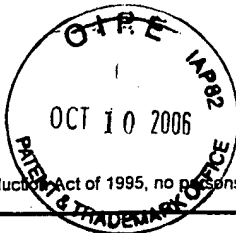
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		EP 1069509 A2*	01-17-2001	Texas Instruments Incorporated		

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

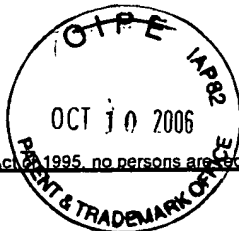
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		*PCT International Search Report (June 27, 2001) 4 pages	
		*Microsoft Press Computer Dictionary, 3rd Ed., 1997, pp. 48 and 68	
		*GUSTAVSON et al., "Macintosh HD:Desktop Folder:SLDRAMrepeaterConcerns", Sunday, November 23, 1997, 4 pages	

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Sheet 1

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Complete if Known

Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-5742840*	04-21-1998	Hansen et al.	
		US-5748872*	05-05-1998	Norman	
		US-5860080*	01-12-1999	James et al.	
		US-5867422*	02-02-1999	John	
		US-5898863*	04-27-1999	Ofer et al.	
		US-6006318*	12-21-1999	Hansen et al.	
		US-6034878*	03-07-2000	Osaka et al.	
		US-6038682*	03-14-2000	Norman	
		US-6092229*	07-18-2000	Boyle et al.	
		US-6125419*	09-26-2000	Umemura et al.	
		US-6151648*	11-21-2000	Haq	
		US-6154826*	11-28-2000	Wulf et al.	
		US-6160423*	12-12-2000	Haq	
		US-6255859*	07-03-2001	Haq	
		US-6263413*	07-17-2001	Motomura et al.	
		US-6327205*	12-04-2001	Haq	
		US-6408402*	06-18-2002	Norman	
		US-6487102*	11-26-2002	Halbert et al.	

FOREIGN PATENT DOCUMENTS

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

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STATEMENT BY APPLICANT**

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Sheet 2

of 4

Complete if Known

Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

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		Number-Kind Code ² (if known)			
		US-6502161*	12-31-2002	Perego et al.	
		US-6513080*	01-28-2003	Haq	
		US-6154855*	11-28-2000	Norman	
		US-6345321*	02-05-2002	Litaize et al.	
		US-6369605*	04-09-2002	Bonella et al.	
		US-6449213*	09-10-2002	Dodd et a.	
		US-6625687*	09-23-2003	Halbert et al.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

3

of

4

Complete if Known

Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,025, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/883,474, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/882,999, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/859,438, filed 05-31-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/858,850, filed 05-31-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/859,060, filed 05-31-2004 by Pete D. Vogt	
		*NG, R. (Sun Microsystems Inc.), "Fast computer memories", IEEE Spectrum, October 1992, pp. 36-39	
		*SALTERS, R.H.W. (Philips Research Laboratories), "Fast DRAMS for sharper TV", IEEE Spectrum, October 1992, pp. 40-42	

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Considered

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		Application Number	10/766,131
		Filing Date	January 28, 2004
		First Named Inventor	Perego
		Art Unit	2188
		Examiner Name	
Sheet 4	of 4	Attorney Docket Number	RAMB-01049US2

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		*JONES, F (United Memories, Inc.), "A new era of fast dynamic RAMs", IEEE Spectrum, October 1992, pp. 43-49	
		*FARMWALD, M. et al. (Rambus Inc.), "A fast path to one memory", IEEE Spectrum, October 1992, pp. 50-51	
		*GJESSING, S. (University of Oslo), GUSTAVSON, D.B (Stanford Linear Accelerator Center), JAMES, D. et al. (Apple Computer Inc.), WIGGER, H. (Hewlett-Packard Co.), "A RAM link for high speed", IEEE Spectrum, October 1992, pp. 52-53	
		*The Institute of Electrical and Electronics Engineering, Inc., "IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)", 1996, pp. 1-91	

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Sheet 1

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Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

U. S. PATENT DOCUMENTS

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		Number-Kind Code ² (if known)			
		US-6425064 B2*	07-23-2002	Soderquist	
		US-4621339*	11-04-1986	Wagner et al.	
		US-4747070*	05-24-1988	Trottier et al.	
		US-4858107*	08-15-1989	Fedele	
		US-5602780*	02-11-1997	Diem et al.	
		US-5642444*	06-24-1997	Mostafavi	
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		US-6708248*	03-16-2004	Garrett, Jr., et al.	
		US-5982238*	11-09-1999	Soderquist	
		US-6639820 B1	10-28-2003	Khandekar et al.	
		US-6820163 B1	11-16-2004	McCall et al.	
		US-6553450 B1	04-22-2003	Dodd et al.	
		US-6510503	01-21-2003	Gillingham et al.	
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FOREIGN PATENT DOCUMENTS

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		EP 0 282 070 B1*	08-03-1994	Fujitsu Limited		

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	10/766,131
				Filing Date	January 28, 2004
				First Named Inventor	Perego
				Art Unit	2188
				Examiner Name	
				Attorney Docket Number	RAMB-01049US2
Sheet	2	of	2		

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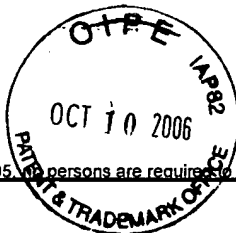
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		US-6272609 B1*	08/07/2001	Jeddeloh	
		US-6292877 B1*	09/18/2001	Ryan	
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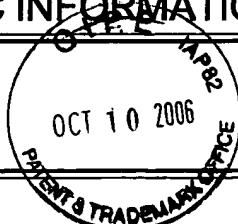
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

**Title of
Invention****CONFIGURABLE WIDTH BUFFERED MODULE**

Application Number: 10/766131

10/766131

Confirmation Number: 3149

First Named Applicant: Richard Perego

Attorney Docket Number: RAMB-01049US2

Art Unit: 2188

Search string: (5513377 or 6526469 or 6185654 or 6530033 or 5228132).pn.

US Patent Documents**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

CONFIGURABLE WIDTH BUFFERED MODULE

Application Number: 10/766131

10/766131

Confirmation Number: 3149

First Named Applicant: Richard Perego

Attorney Docket Number: RAMB-01049US2

Art Unit: 2188

Search string: (6330687 or 5371880 or 5475818 or 5400360 or 4631666 or 5655113 or 5511224 or 5574945 or 5917760 or 6016282 or 6449727 or 4644532 or 4667305 or 4747100 or 4864563 or 5228134 or 5355467 or 5408646 or 5537394 or 5544342 or 5553266 or 5557266 or 5604735 or 5630095 or 5659710 or 5787083 or 5802054 or 5802565 or 5805798 or 5848145 or 5884036 or 5893921 or 5900017 or 5901294 or 5911052 or 5913044 or 5923893 or 6247100 or 20040095838 or 20040260991 or 20040250181 or 20040250153 or 20040150024 or 20040246786 or 20040246785 or 20040246767 or 20040015650 or 20030177313 or 20020083287).pn.

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Remarks

Note: Remarks are not for responding to an office action.

An Information Disclosure Statement containing 5 Foreign Patent Document and 3 Non Patent Literature Documents is being filed concurrently, via US mail. A fee for both Information Disclosure Statements is included with this submission.

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